



PATENT APPLICATION  
Express Mail Label No. *EL960827955US*  
Attorney Docket No. SUN-P9610-SPL

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

UTILITY PATENT  
APPLICATION TRANSMITTAL LETTER

Asst. Commissioner for Patents  
Box Patent Application  
Washington, D.C. 20231

Sir:

Enclosed for filing is an ☒ original patent application or, ☐ a continuation-in-part patent application, by inventor(s) Ivan E. Sutherland, Robert J. Drost, Gary R. Lauterbach, Howard L. Davidson, entitled INTEGRATED CIRCUIT ASSEMBLY MODULE THAT SUPPORTS CAPACITIVE COMMUNICATION BETWEEN SEMICONDUCTOR DIES.

No. of pages in Application: 22; No. of Claims: 36.

No. of Sheets of Drawings: Formal: 2, Informal: 0.

Also enclosed are:

- ☐ a claim for foreign priority under 35 U.S.C. §§ 119 and/or 365 in
- ☐ a separate document ☐ the declaration;
- ☐ a certified copy of the priority document;
- ☐ an Associate Power of Attorney;
- ☐      verified statement(s) claiming small entity status;
- ☒ a Combined Declaration and Power of Attorney of the inventors(s);
- ☐ a signed Combined Declaration and Power of Attorney of the inventors will follow;
- ☒ an Assignment document and form PTO-1595;
- ☒ Request And Certification Under 35 U.S.C. 122(B)(2)(B)(I); and
- ☒ Information Disclosure Statement and Form PTO-1449 with one reference.

The fee has been calculated as follows:

CLAIMS					
	NO. OF CLAIMS		EXTRA CLAIMS	RATE	FEE
Basic Application Fee					\$750.00
Total Claims	36	MINUS 20 =	16	\$18.00=	\$288.00
Independent Claims	3	MINUS 3 =	0	\$84.00=	\$0.00
If multiple dependent claims are presented, add \$260.00					0
Total Application Fee					\$1,038.00
If verified statement claiming small entity status is enclosed, subtract 50% of Total Application Fee					
Add Recording Fee of \$40.00 if Assignment document is enclosed					\$40.00
<b>TOTAL APPLICATION FEE DUE</b>					<b>\$1,078.00</b>

- ☒ A check in the amount of \$ 1,078.00 is enclosed.
- ☐ Application fee will follow with missing parts.
- ☒ Please deduct any underpayments, credit any overpayments, and charge all required extension of time fees to Deposit Account Number 50-1003.

Please direct all correspondence concerning the above-identified application to the following address:

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22835

PATENT TRADEMARK OFFICE

Respectfully submitted,

By

A. Richard Park  
Registration No. 41,241

Date: September 26, 2003

<b>REQUEST AND CERTIFICATION UNDER 35 U.S.C. 122(b)(2)(B)(i)</b>		First Name of Inventor	Ivan E. Sutherland et al.
		Title	INTEGRATED CIRCUIT ASSEMBLY MODULE THAT SUPPORTS CAPACITIVE COMMUNICATION BETWEEN SEMICONDUCTOR DIES
		Attorney Docket Number	SUN-P9610-SPL

I hereby certify that the invention disclosed in the attached application **has not and will not be** the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing. I hereby request that the attached application not be published under 35 U.S.C. 122(b).

September 26, 2003

Date

  
Signature

A. Richard Park (Reg. No 41,241)

Typed or printed name

This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application **upon filing**.

Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.

If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing, the applicant must notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application. **Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(iii)).**